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Apple III OEM Prototyping Card: Specifications (Discontinued)

Revised: 9/13/93
Security: Everyone

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Circuit Development Area (A)

- 5.40-inch by 3.70-inch space
- 0.10-inch hole spacing
- Accepts up to 42 14-pin; 36 16-pin; 24 24-pin; or 12 40-pin DIP packages or combinations thereof

Input Buffering Area (B)

This area is for installation of RFI isolation circuitry.

- Connector Interface Areas:
 - Area C: for up to 67-pin, "D"-type connectors
 - Area D: for up to 50-pin, ribbon-cable connectors
- Additional Board Areas:
 - SPST switch position (E)
 - RF Shield (supplied) connection points (F)
 - 50-pin, gold-plated edge connector

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